

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Serial No .: 09/450,261

Filed: November 29, 1999

**AUTOMATICALLY ENABLING INFORMATION** For: TO BE DISPLAYED AFTER A PROCESSOR-

**BASED SYSTEM IS TURNED OFF** 

Box Non-Fee Amendment Commissioner for Patents

Group Art Unit:

Examiner:

Kenny S. Lin

2154

Atty. Dkt. No.:

ITL.0289US

(P7820)

## **RESPONSE TO OFFICE ACTION DATED AUGUST 19, 2002**

In response to the Office Action mailed August 19, 2002, reconsideration is respectfully requested in view of the following amendments and remarks.

In the Specification:

Washington, D.C. 20231

Please amend the specification as follows:

On page 10, lines 7-16:

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**Technology Center 2100** 

Turning finally to Figure 7, the processor-based system 10 may include a processor 60 coupled to an interface 62 such as a bridge. The interface 62 may be coupled to a system memory 64 and a display controller 66. A display 68 may be coupled to the display controller 66. An interface 70 may be coupled to the interface 62 as well as to a system bus 72. The real time clock may be part of the interface 70. The system bus 72 may be coupled to a storage medium 74, storing the software 76 for example that implements the PIM functions and the software 24.

## In the Claims:

Please amend the claims as follows:

(Amended) The method of claim 1 wherein said time sensitive data is automatically transferred from the storage coupled to the first processor-based system when it is determined that the first processor-based system is being powered off.

> Date of Deposit: **NOVEMBER 15, 2002**

I hereby certify under 37 CFR §1.8(a) that his correspondence is being deposited with the United States Postal Service as First Class Mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

Ella Sisco